

Notice of References Cited	Application/Control No. 10/583,397		Applicant(s)/Patent Under Reexamination XIONG, XIAOKUN	
	Examiner BRIAN T. MISIURA		Art Unit 2111	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,996,659	02-2006	Lupien et al.	710/315
*	B	US-5,603,051	02-1997	Ezzet, Ali	710/2
*	C	US-6,829,669	12-2004	Jahnke et al.	710/306
*	D	US-6,789,153	09-2004	Stewart, Charles H.	710/306
*	E	US-7,165,184	01-2007	Fuks, Adam	713/400
*	F	US-6,442,642	08-2002	Brooks, John Milford	710/305
*	G	US-6,571,308	05-2003	Reiss et al.	710/315
*	H	US-6,161,161	12-2000	Botkin et al.	710/313
*	I	US-6,122,670	09-2000	Bennett et al.	709/236
*	J	US-6,112,241	08-2000	Abdelnour et al.	709/224
*	K	US-6,065,087	05-2000	Keaveny et al.	710/315
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	M	US-			

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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	ARM - "AMBA Specification Revision 2.0" - 230 pages; Dated 1999			
	V	BaBar Electronics - "BaBar DAQ System - PCI Mezzanine Card, i960 Signals" - 6 pages; Dated 3/5/1997			
	W	Intel - "Intel i960 Processors" - 4 pages; Dated 2002			
	X	"The i960CA SuperScalar Implementation of the 80960 Architecture" - 9 pages; Dated 1990			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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	U	"Design and Implementation of the STAR Experiment's DAQ" - 6 pages; No Date Provided
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	W	
	X	

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